

App. No. 10/501,865
Office Action Dated September 8, 2005

Amendments to the Claims:

This listing of claims will replace all prior versions and listing of claims in the application.

Claims 1-7, 11-13, and 15 are canceled.

Listing of Claims:

1-7. (Canceled)

8. (Previously Presented) A MIM capacitor comprising:
- a substrate;
 - a first metal area formed opposing the substrate;
 - a second metal area formed opposing the substrate, the second metal area being coplanar with the first metal area;
 - a third metal area formed between the first metal area and the substrate so as to oppose the first metal area;
 - a fourth metal area formed between the second metal area and the substrate so as to oppose the second metal area, the fourth metal area being coplanar with the third metal area; and
 - an insulating film formed between the first metal area and the third metal area, and between the second metal area and the fourth metal area;
- wherein a first capacitance value is determined by opposing surface areas of the first metal area and the third metal area, and a second capacitance value is determined by opposing surface areas of the second metal area and the fourth metal area;
- and further comprising a fifth metal area formed in an electrically floating state between the third and fourth metal areas and the substrate so as to oppose both the third metal area and the fourth metal area.
9. (Original) The MIM capacitor according to claim 8, wherein the fifth metal area is connected to a ground potential.

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10. (Original) The MIM capacitor according to claim 9, wherein the fifth metal area is connected to the ground potential at a connection point such that impedance to the third metal area and impedance to the fourth metal area are substantially equivalent.

11-13. (Canceled)

14. (Previously Presented) A MIM capacitor comprising:
a substrate;
a first metal area formed opposing the substrate;
a second metal area formed opposing the substrate, the second metal area being adjacent to the first metal area;
a third metal area formed between the first metal area and the substrate so as to oppose the first metal area;
a fourth metal area formed between the second metal area and the substrate so as to oppose the second metal area, the fourth metal area being adjacent to the third metal area; and
an insulating film formed between the first metal area and the third metal area, and between the second metal area and the fourth metal area;
wherein a first capacitance value is determined by opposing surface areas of the first metal area and the third metal area, and a second capacitance value is determined by opposing surface areas of the second metal area and the fourth metal area;
and further comprising a fifth area formed as a diffusion layer having conductivity in an electrically floating state between the third and fourth metal areas and the substrate so as to oppose both the third metal area and the fourth metal area.

15. (Canceled)